## **EXHIBIT** A

## **'273 PATENT**

## INFRINGEMENT EVIDENCE

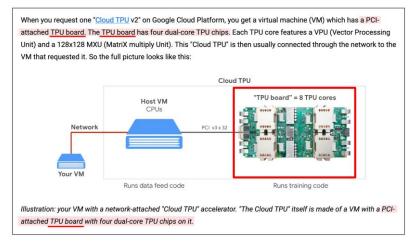
## 53. A device:

comprising at least one first low precision high-dynamic range (LPHDR) execution unit adapted to execute a first operation on a first input signal representing a first numerical value to produce a first output signal representing a second numerical value,

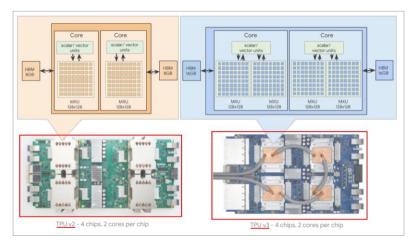
wherein the dynamic range of the possible valid inputs to the first operation is at least as wide as from 1/1,000,000 through 1.000.000 and for at least X=5% of the possible valid inputs to the first operation, the statistical mean, over repeated execution of the first operation on each specific input from the at least X% of the possible valid inputs to the first operation, of the numerical values represented by the first output signal of the LPHDR unit executing the first operation on that input differs by at least Y=0.05% from the result of an exact mathematical calculation of the first operation on the numerical values of that same input;

wherein the number of LPHDR execution units in the device exceeds by at least one hundred the non-negative integer number of execution units in the device adapted to execute at least the operation of multiplication on floating point numbers that are at least 32 bits wide.

As demonstrated below, the Accused Products include multiple components that, separately and independently, meet all the requirements of the claimed "device." For example, a "TPU Board" satisfies these requirements:



https://codelabs.developers.google.com/codelabs/keras-flowers-convnets/#2



https://cloud.google.com/tpu/docs/system-architecture